

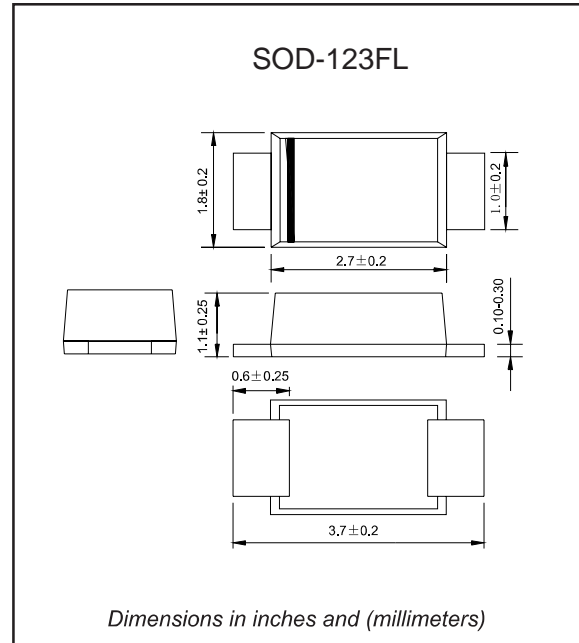
Features

- ▶ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ▶ For surface mounted applications
- ▶ Low reverse leakage
- ▶ Built-in strain relief, ideal for automated placement
- ▶ High forward surge current capability
- ▶ High temperature soldering guaranteed: 260°C/10 seconds at terminals
- ▶ Glass passivated chip junction

Mechanical data

- ▶ **Case:** JEDEC SOD-123FL molded plastic body
- ▶ **Terminals:** Solder plated, solderable per MIL-STD-750, Method 2026
- ▶ **Polarity:** Color band denotes cathode end
- ▶ **Mounting Position:** Any

Package outline



Maximum ratings and Electrical Characteristics (AT $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOLS	ES1AFL	ES1BFL	ES1CFL	ES1DFL	ES1EFL	ES1GFL	ES1JFL	UNITS
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	150	200	300	400	600	V
Maximum RMS voltage	V_{RMS}	35	70	105	140	210	280	420	V
Maximum DC blocking voltage	V_{DC}	50	100	150	200	300	400	600	V
Maximum average forward rectified current at $T_L=75^\circ\text{C}$	$I_{(AV)}$	1.0							A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load	I_{FSM}	30							A
Maximum instantaneous forward voltage at 1.0A	V_F	0.95			1.25		1.70		V
Maximum DC reverse current $T_A=25^\circ\text{C}$ at rated DC blocking voltage $T_A=100^\circ\text{C}$	I_R	5.0 50.0							μA
Maximum reverse recovery time (NOTE 1)	t_{rr}	35							ns
Typical junction capacitance (NOTE 2)	C_J	10.0							pF
Typical thermal resistance (NOTE 3)	$R_{\theta JA}$	85.0							$^\circ\text{C/W}$
Operating junction and storage temperature range	T_J, T_{STG}	-55 to +150							$^\circ\text{C}$

- Note:** 1.Reverse recovery condition $I_F=0.5\text{A}, I_R=1.0\text{A}, I_{rr}=0.25\text{A}$
 2.Measured at 1MHz and applied reverse voltage of 4.0V D.C.
 3.P.C.B. mounted with 2.0x2.0"(5.0x5.0cm) copper pad areas

Rating and characteristic curves

FIG.1-TYPICAL FORWARD CHARACTERISTICS

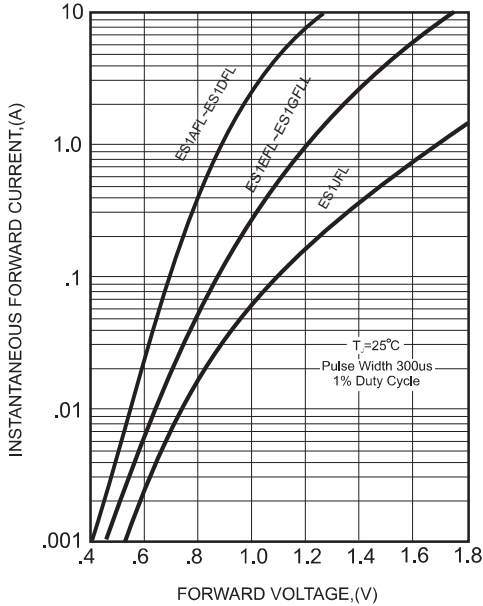


FIG.2-TYPICAL FORWARD DERATING CURVE

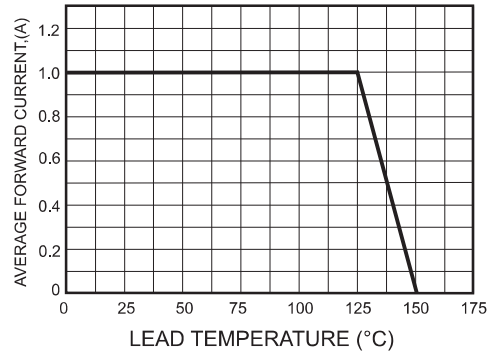
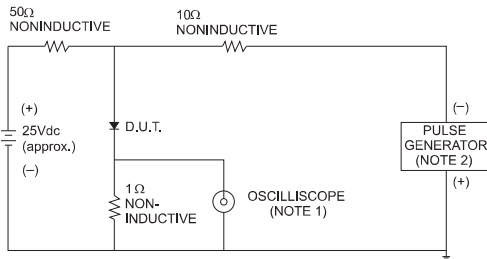


FIG.3- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTICS



NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm.22pF.
2. Rise Time= 10ns max., Source Impedance= 50 ohms.

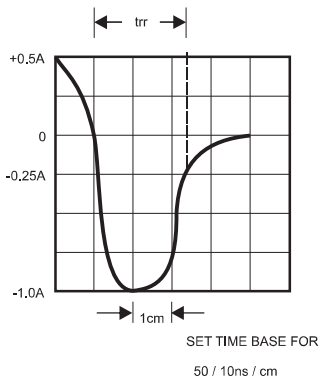


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

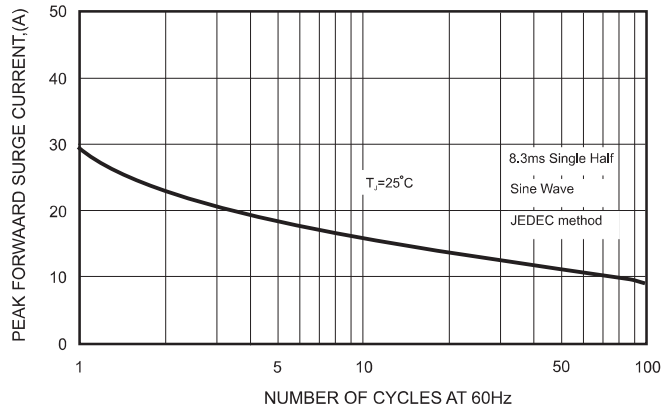
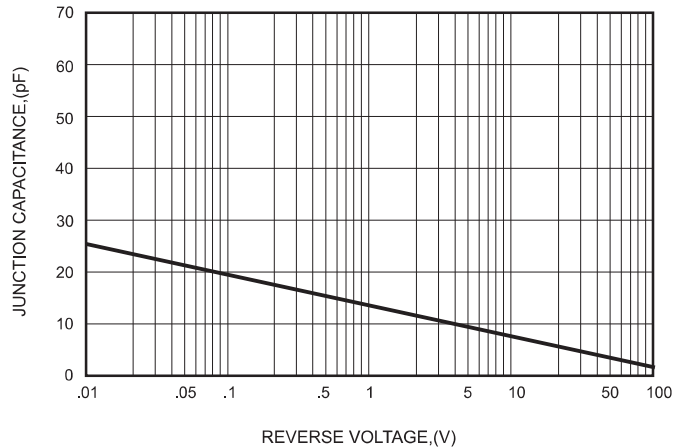




FIG.5-TYPICAL JUNCTION CAPACITANCE



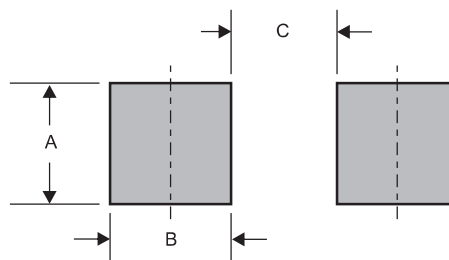
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code
ES1AFL	E1A
ES1BFL	E1B
ES1CFL	E1C
ES1DFL	E1D
ES1EFL	E1E
ES1GFL	E1G
ES1JFL	E1J

Suggested solder pad layout

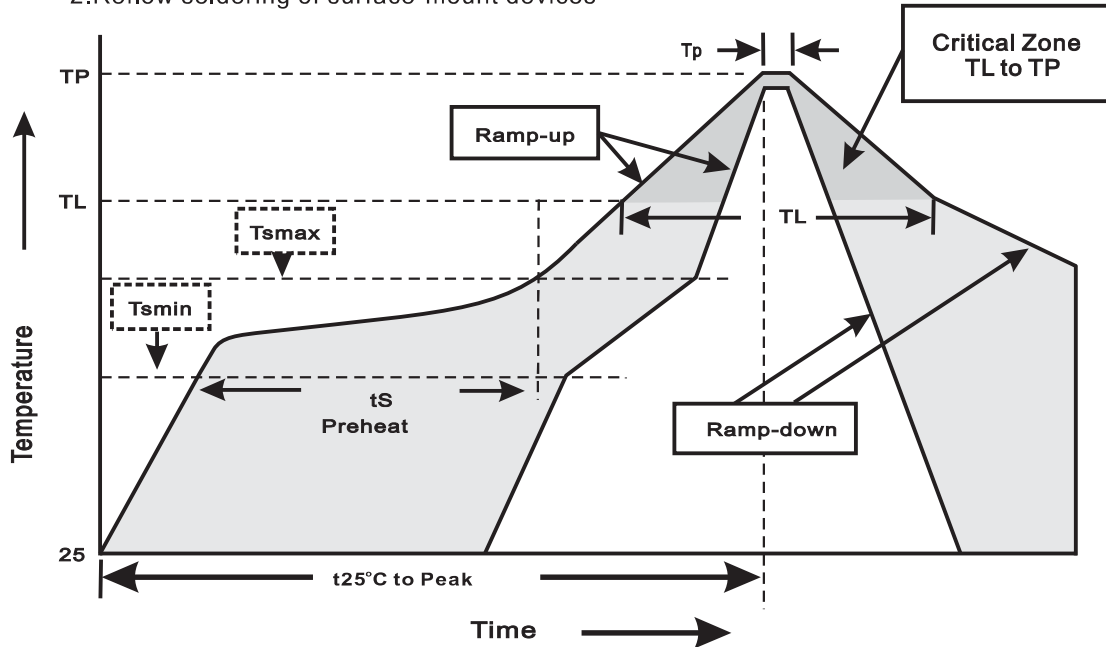


Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SOD-123FL	0.075 (1.90)	0.055 (1.40)	0.075 (1.90)

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{smmin}) -Temperature Max(T _{smmax}) -Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smmax} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _p)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes