



Schematic for the AR0135 Evaluation Board



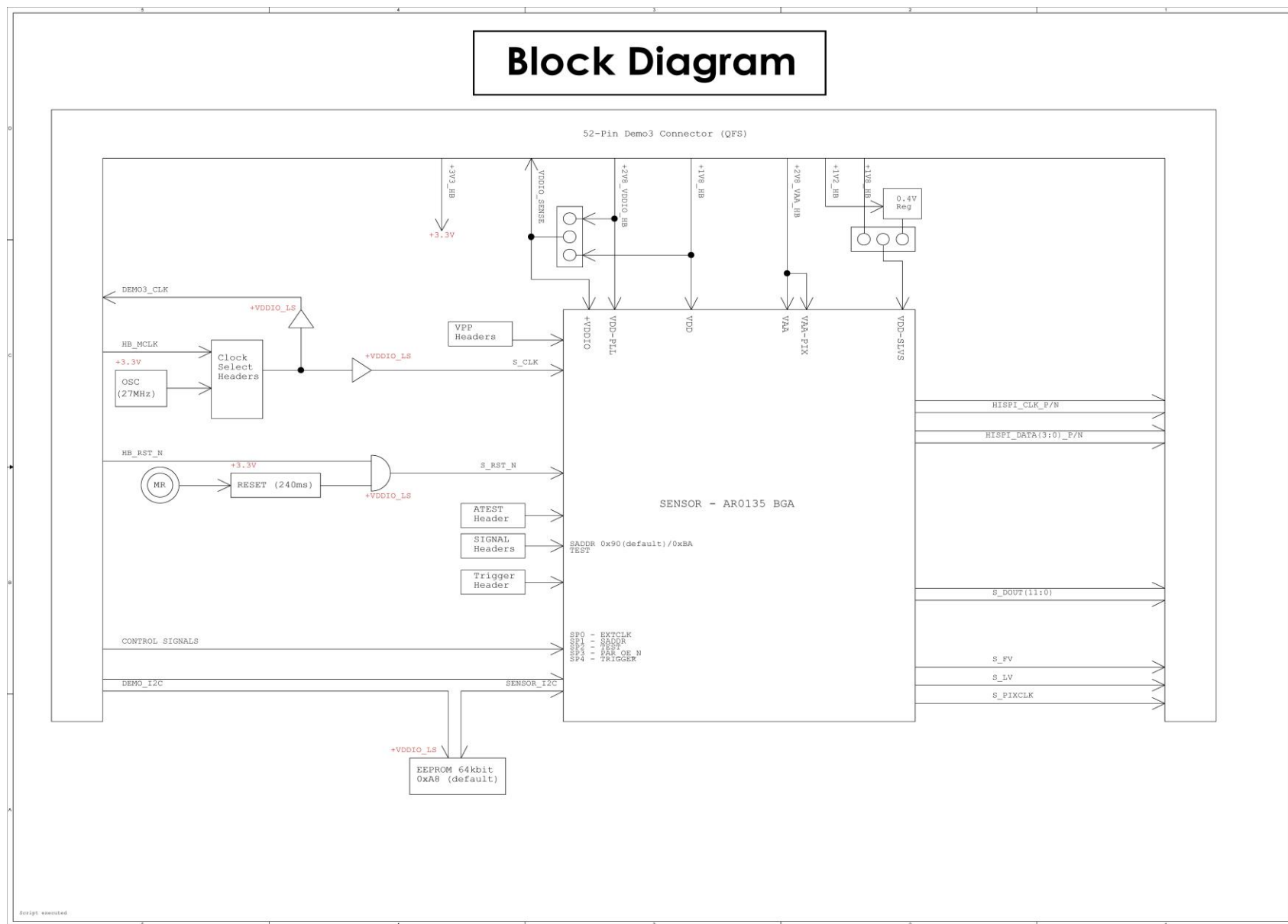
AR0135_BGA_Demo3Head

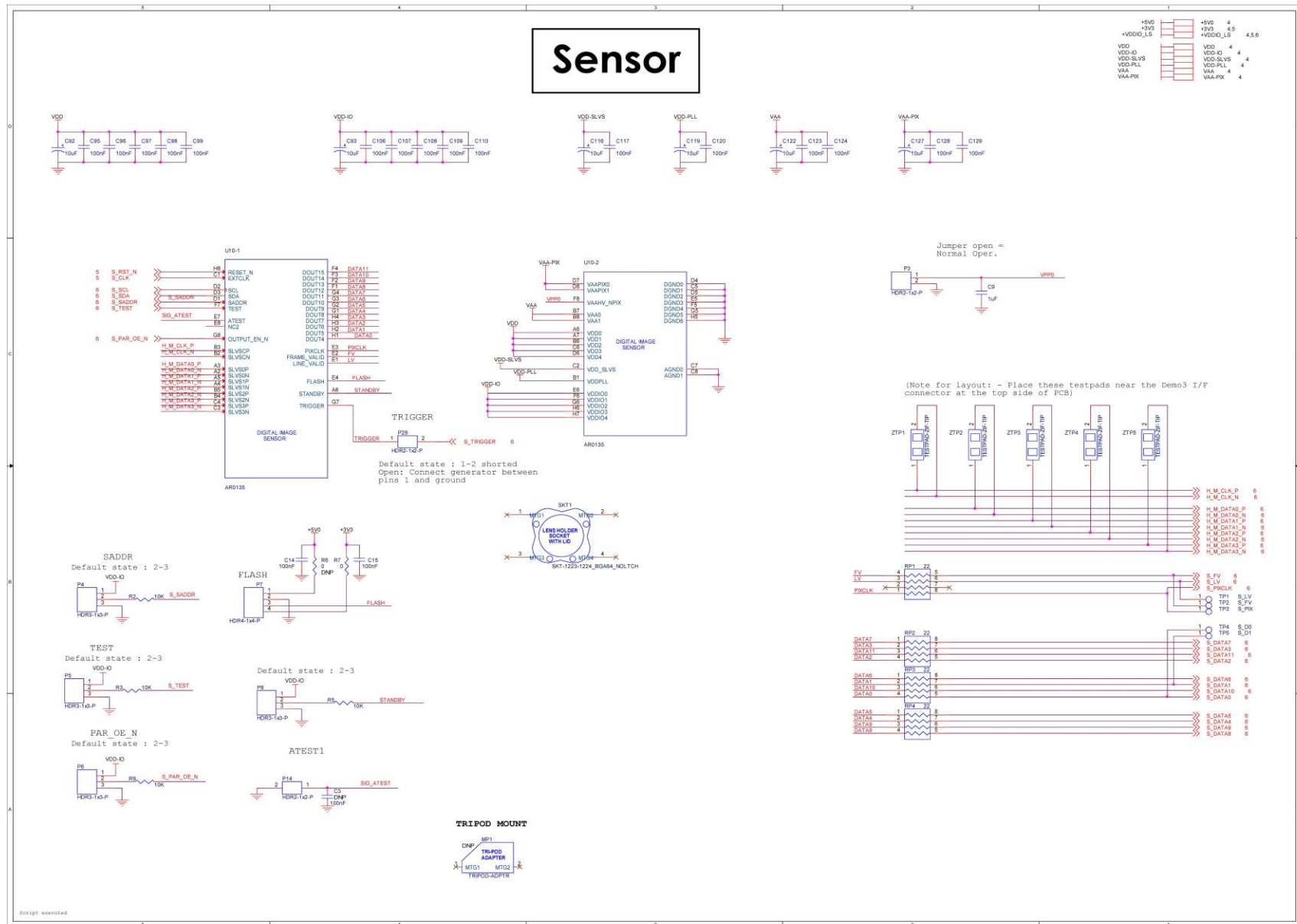
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Rev	Who	Date	Description
Rev 0.0	aralex	05/07/14	Initial
Rev 0.0	aralex	05/08/14	Removed the 10nF Decaps on VDD and VDDIO for Sensor
		05/13/14	Updated the sensor with the new non socketed part, and added C-mount lens
		05/15/14	Updated the sensor with the new non socketed part, and added the socket for demo3 board
Rev 0.1	aralex	05/16/14	Removed text near R8 for TRIGGER signal Replaced R25 with a fixed resistor of 20K
	aralex	05/20/14	Removed Skip Block Fiducials FD4 and FD8.
Rev 1.0	aralex	04/23/15	Added a 2 pin header P28 for Trigger signal, deleted R8



Block Diagram



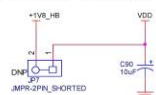




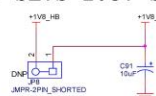
Power

Debug Headers: Cut away the shorted trace
and mount header for power debugging

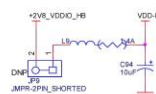
VDD 1.8V SUPPLY



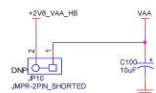
VDD-SLVS 1.8V SUPPLY



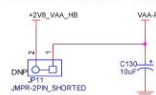
VDD-PLL 2.8V SUPPLY



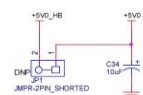
VAA 2.8V SUPPLY



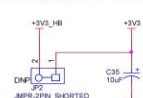
VAA-PIX 2.8V SUPPLY



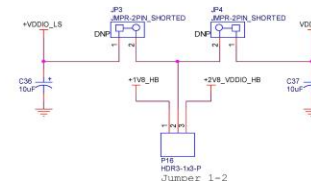
PERIPHERAL 5V SUPPLY



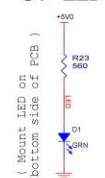
PERIPHERAL 3.3V SUPPLY



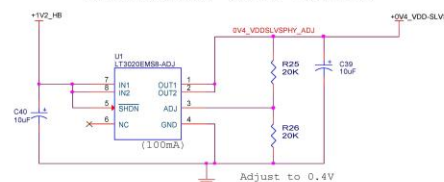
VDDIO & VDDIO LS 1.8V/2.8V SUPPLY



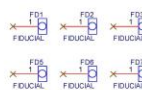
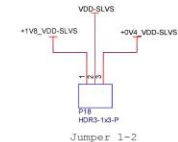
5V LED



VDDSLVSPHY 0.4V SUPPLY



Selection of 0.4V or 1.2V/1V8
for VDDSLVSPHY supply



Ground Testpoints



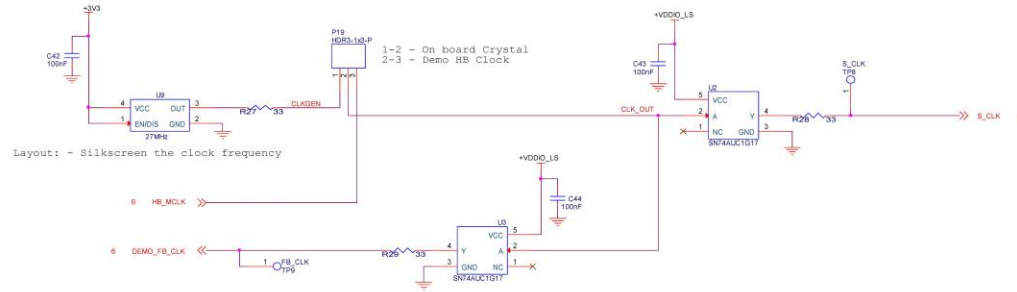
Mounting Holes



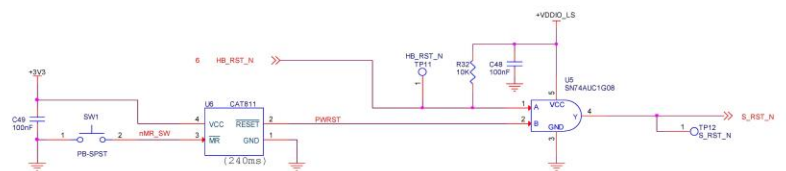


Clock and Reset

CLOCK CIRCUIT



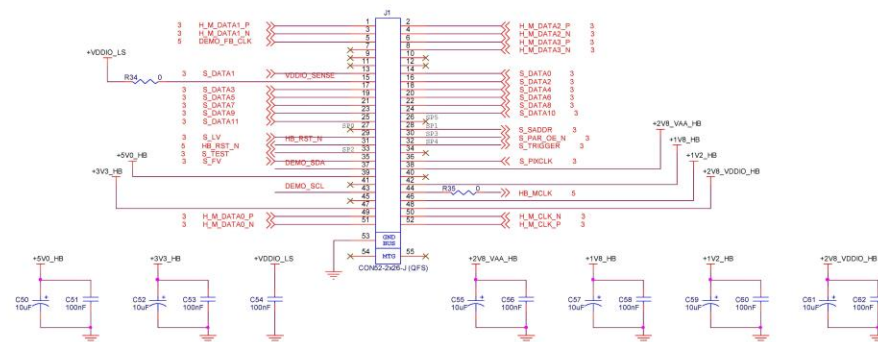
RESET CIRCUIT



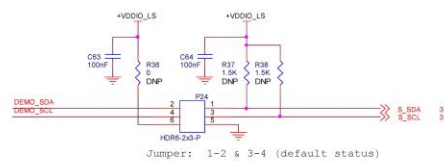


External Interface

DEMO3 BASEBOARD I/F



I2C DEBUG



LENS CORRECTION EEPROM

